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| (X ₁ /X ₂) - (M/M × 11 X − 1 → X SP − 1 → SP X × 1 → X | 2P+1+SP IL→XH, (IN+1)→X ₁ II→SP ₁ , (IN+3)→SP ₁ | XH - M. X - (M + 1) | SPH M, SPL (M + 1) | X-1-65 | | BRANCH TEST | None | 0-0 | 1 - 2 | Nev-0 | 0 - 2 + 3 | 2 + (N o VI = 1 | Nov-1 | N=1 Z=0 | 0 * A | 1 . 7 | 1 | See Special Operations | Adonces Pros Cete, Only | | See special Operations | | COMBITTION CODE REGISTER NOTES: | (Bit V) Test: Result = 100020007 | | (Not cleared if previously set.) | Test: Operand - 01111111 prior to execution? | Test: Sign bit of most significant (MS) byte of sesuit = 17 | | Load Condition Code Register from Stack, (See Special Operations) | Set when interupt occurs, it previously set, a Ron-Masuable Interupt is required to exit the wait teste. | Set according to the contents of Accomulator A. | | | | | | | |
| | - | | | | = | 12 | | | | | | | | | | | 1 | | - | - | | | OJ NC | (Bar V) | | (0.00) | | | | | igu si | MILLI | | | | | | | |
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| n | | 1 | | | | MNEMONIC | BAA | BCC | 86.0 | BOE | BH B | 976 | 61.7 | BANE | BVC | BVS | BSH | 150 | N09 | E | SWI | | 1 | 2 ' | 2 | ~ . | - 2 | ~ ~ | | 0 | z _ | 2 | 2 | - 44 | œ , | | | 5 9 | 8 |
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| DEX DEX INX | LOX | XTX | 212 | TSX | | | | | | | | | | | | | | | | | | | MATMONIC | 2112 | CIV | 285 | SEV SEV | TPA | | | Operation Code (Hexadecimal); Number of MPU Cycles; | gram Byter, | 9 | 40. | thory location tack Pointer; | ve 0.R; | ING DR. | | |
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| Compari Decrema Decrema | Lead In | Store in | Store St | Stack Pr | JUMP A | DPERA | Branch | Branch | Branch | Branch | Branch | Branch | Branch | Branch | Branch | Branch | Branch | Jump Jump T | No Operation | Return | Solivest Wait for | 1 | OPERA | Clear Carry | Clear 0 | Set Carry | Set Describon | CCR - | | 27 6 | 1 6 | 10 | + | | dS.M | + | 9 12 | E 2 | 0 |

